PTO/SB/30 (10-015

	er the Paperwork Reduction Act of 1995, no persons are required to response to the PEOLICET	Application Number	09/902,672 √
	REQUEST	Filing Date	July 12, 2001
<u>~</u>	FOR		
CONTINUED EXAMINATION (RCE)		First Named Inventor	M. Funabashi
5	TRANSMITTAL	Art Unit	2825
2003 🗒	Address to: Commissioner for Patents	Examiner Name	L. Malsawma
Z.	Box RCE Washington, DC 20231	Attorney Docket Number	843.37558VX2
1995, or	to r Continued Examination (RCE) practice under 37 CFR 1.11- to any design application. See Instruction Sheet for RCEs (not submission required under 37 C.F.R. § 1.114 Previously submitted i Consider the amendment(s)/reply under 37 C. (Any unentered amendment(s) referred to above will be er ii Consider the arguments in the Appeal Brief or iii Other Enclosed i Amendment/Reply	to be submitted to the USPTO) on .F.R. § 1.116 previously filed on tered).	page 2.
a. b.	scellaneous Suspension of action on the above-identified appliperiod of months. (Period of suspension shall no Other	t exceed 3 months; Fee under 37 C.F.R.	§ 1.17(i) required)
3. Fea.b.c.	The RCE fee under 37 C.F.R. § 1.17 (e) is required by 37 (c) The Director is hereby authorized to charge the following peposit Account No. 01-2135 (Case No. 843.375) RCE fee required under 37 C.F.R. § 1.17 (e) Extension of time fee (37 C.F.R. §§ 1.136 and iii Other Check in the amount of \$ enclosed Payment by credit card (Form PTO-2038 enclosed)	llowing fees, or credit any overp <u>58VX2)</u> I 1.17)	103 103 103 103 103 103 103 103 103 103
	WARNING: Information on this form may become included on this form. Provide credit card	ome public. Credit card information and sutherization	nation should not
₹ -		TTORNEY, OR AGENT REQUIRED	
	Print/Type) William I. Solomor	Registration No. (Attorney/Age	
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Burden Hour Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231, DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND Fees and Completed Forms to the following address: Assistant Commissioner for Patents, Box RCE, Washington, DC 20231.

Date

May 16, 2003

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Signature

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01 FC:1801

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

Serial No.:

09/902,672

Filed:

July 12, 2001

For:

METHOD OF MANUFACTURING A SEMICONDUCTOR

INTEGRATED CIRCUIT DEVICE

Group:

2825

Examiner:

L. Malsawma

SUBMISSION UNDER 37 CFR § 1.114

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

Filed concurrently with the Request for Continued Examination (RCE) Transmittal for the above-identified application, and as the necessary Submission

therefor, please amend the above-identified application as follows.

IN THE CLAIMS

Please amend the claims presently in the application as follows:

- 17. (Amended) A method of manufacturing a semiconductor integrated circuit device, comprising the steps of:
- providing a silicon wafer covered with an insulating film whose main surface is mainly formed of silicon oxide;

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36.00 OP

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